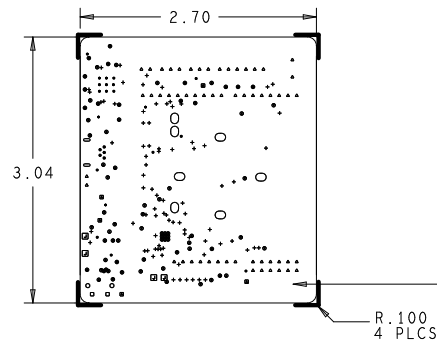


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	ALPHA RELEASE	09/12/13	PAVA
02	BETA RELEASE	10/10/13	PAVA
03	OOB RELEASE	11/26/13	PAVA

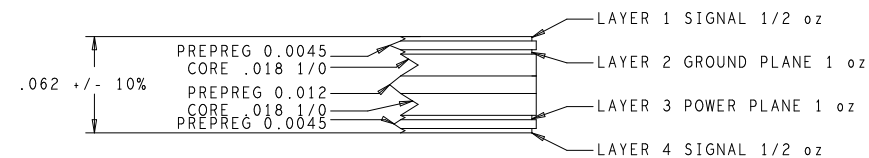


DRILL CHART: MBR3				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
+	10.0	+3.0/-3.0	PLATED	82
•	13.0	+3.0/-3.0	PLATED	75
•	18.0	+3.0/-3.0	PLATED	20
•	28.0	+3.0/-3.0	PLATED	5
■	35.433	+0.0/-0.0	PLATED	4
°	39.0	+3.0/-3.0	PLATED	2
•	40.0	+3.0/-3.0	PLATED	56
■	43.0	+3.0/-3.0	PLATED	5
°	51.0	+3.0/-3.0	PLATED	2
=	74.799x27.598	+3.0/-3.0	PLATED	2
○	120.0x90.0	+3.0/-3.0	NON-PLATED	3
○	120.0x90.0	+3.0/-3.0	NON-PLATED	4

DATE CODE & UL MARKING
SILKSCREEN BOTTOM

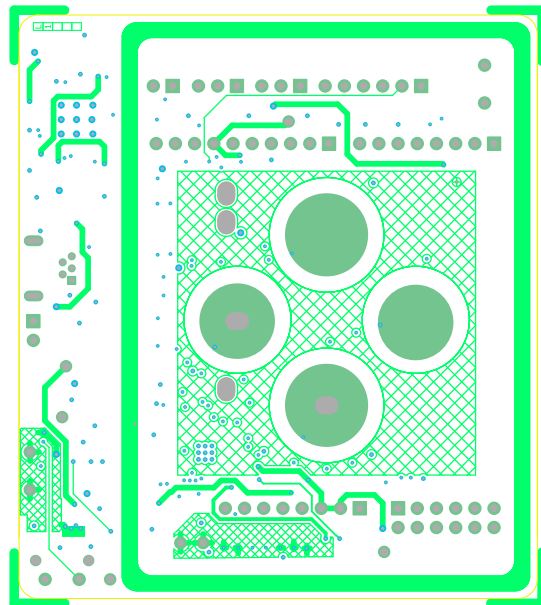
NOTES: (UNLESS OTHERWISE SPECIFIED)

- RoHS COMPLIANT CERTIFICATION OR MATERIAL DECLARATION REQUIRED
- MATERIAL:
 - IS410 OR EQUIVALENT. MATERIAL MUST CONFORM TO UL94V-0.
 - USE HTE COPPER, AS SPECIFIED IN THE CROSS SECTION DIAGRAM
 - OVERALL METAL TO METAL THICKNESS AS SPECIFIED IN THE CROSS SECTION
- DRILLING:
 - DIAMETERS IN DRILL TABLE ARE FINISHED HOLE SIZES $\pm .003$ tol. UNLESS OTHERWISE SPECIFIED IN DRILL TABLE.
 - TEARDROP ALLOWED ON ENTRY OF VIA ON EVERY TRACE LAYER.
- PLATING:
 - COPPER PLATING IN THRU-HOLES .001 min.
- MARKING:
 - SILKSCREEN IN WHITE NON-CONDUCTIVE EPOXY INK ON PRIMARY SIDE OF THE BOARD OR BOTH SIDES IF APPLICABLE.
 - FABRICATOR TO PLACE DATE CODE AND LOGO ON SECONDARY SIDE IN ETCH (PREFERRED) OR SILKSCREEN.
- FINAL FABRICATION:
 - SOLDRMASK PRIMARY AND SECONDARY SIDE OF BOARD USING LIQUID PHOTOIMAGABLE MASK MATERIAL OVER BARE COPPER. PER IPC-SM-840. MASK ARTWORKS PROVIDED ARE 1:1. SOLDERMASK COLOR **BLACK**
- FINISH:
 - SHALL BE ELECTROLYTIC NICKEL/GOLD. ELECTROLESS NIKEL / IMMERSION GOLD. (ENIG)
 - NICKEL THICKNESS: 100-200 MICROINCHES.
 - GOLD THICKNESS: 3-10 MICROINCHES.
- BOARDS SHALL BE PURCHASED FROM UL RECOGNIZED VENDORS ONLY AND SHALL BE MARKED IN COPPER ON SECONDARY SIDE OF BOARD WITH VENDORS UL IDENT. FLAMABILITY RATING (94-V0), DATE CODE (WWYY), AND RoHS COMPLIANT SYMBOL.
- MANUFACTURE BOARD TO BE IN ACCORDANCE WITH PERFORMANCE STANDARD IPC-6011/6012 CLASS 2 BOARD TO BE INSPECTED PER IPC-600-A CLASS 2.
- MAXIMUM WRAP OR TWIST SHALL NOT EXCEED .007 in/in.
- TESTING:
 - FABRICATOR TO ADD TEST STRUCTURES OR CUPONS AS NEEDED.
 - T-LINE IMPEDENCE TO BE TESTED OR GUARANTEE WITHIN 10% IF SPECIFIED IN THE CROSS SECTION. TOLERANCE TO BE $\pm 10\%$ UNLESS OTHERWISE SPECIFIED.

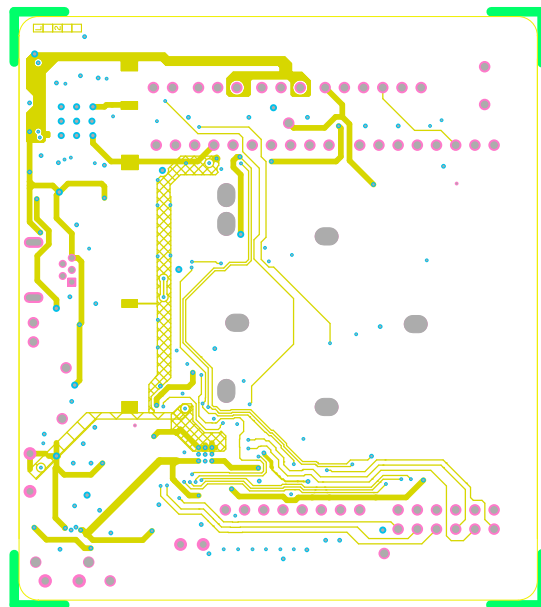


NOTE: 9 MIL WIDTH & 16 MIL SPACING TRACES ON LAYER 4 IS DIFFERENTIAL
IMPEDANCE CONTROLLED TO 90 OHMS $\pm 10\%$ TESTED

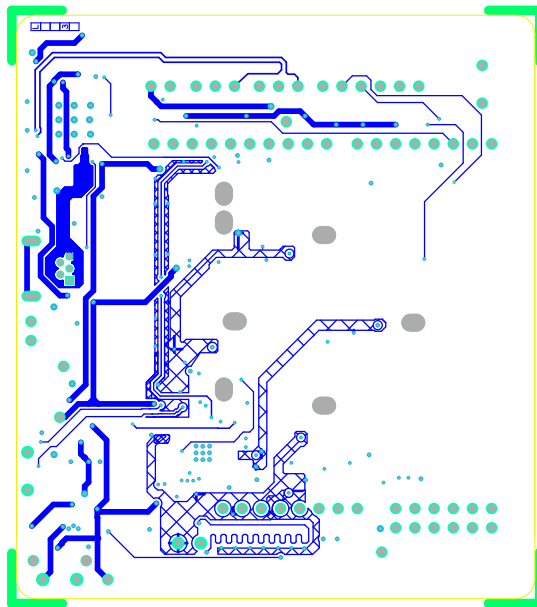
UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE	 198 CHAMPION COURT SAN JOSE, CA 95134 (408) 943-2600
DIMENSIONS ARE IN INCHES	DRAWN PRAO	11/26/13	
TOLERANCES ON: ANGLES $\pm 2^\circ$	CHECKED PAVA	11/26/13	
2 PL DECIMALS $\pm .010$	ENGRG PAVA	11/26/13	
3 PL DECIMALS $\pm .005$	ISSUED		
THIRD ANGLE PROJECTIONS			
CYPRESS PROPRIETARY THIS DOCUMENT CONTAINS CONFIDENTIAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY CYPRESS.			
SIZE C		FSCM NO	DWG NO 610-60165-01
SCALE: 1/1		REV 103	
		SHEET: 1 OF 1	



600-60172-01 REV03 PRIMARY SIDE



600-60172-01 REV03 GROUND LAYER



600-60172-01 REV03 POWER PLANE

600-60175-01 REV03 SECONDARY SIDE

